Offline Electron Seeding Validation - Update

Caleb Fangmeier Ilya Kravchenko, Greg Snow

University of Nebraska - Lincoln

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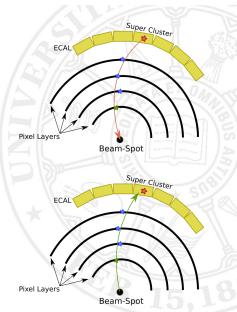


INTRODUCTION

- Our goal is to study seeding for the offline GSF tracking with the Phase I pixel detector.
- Specifically, we want to optimize the new pixel-matching scheme from HLT for use in off-line reconstruction.
- ► This Talk:
 - ► Explain "Hit Skipping" and demonstrate effects on seeding performance
 - Compare performance with pileup added

N-HIT ELECTRON SEEDING

- 1. Using the beam spot, the SC position, and SC energy, propagate a path through the pixels.
- 2. Require the first hit to be within a $\delta \phi$ and δz window. ($\delta \phi$ and δR for FPIX)
- 3. δz window for first hit is huge as SC and beam spot positions give very little information about *z*.
- Forget the SC position, and propagate a new track based on the vertex and first hit positions, and the SC energy.
- Progress one-by-one through the remaining hits in the seed and require each one fit within a specified window around the track.
- Quit when all hits are matched, or a hit falls outside the window. No skipping is allowed.



Hit-Skipping

- ▶ Normally, general tracking seeds are made with an iterative procedure
- Each iteration masks hits from use in subsequent steps
- Reduces combinatorics for CKF tracking

pixelHits ---> Seed Step X Mask Hits —>initialSeeds initialStep highPtTripletStep mixedTripletStep tripletElectronSeeds pixelPairElectronSeeds

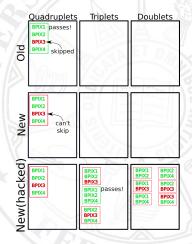
Each iteration works on a specific set of combinations of layers. eg. initialStep uses:

```
BPix1+BPix2+BPix3+BPix4
BPix1+BPix2+BPix3+FPix1_pos
BPix1+BPix2+BPix3+FPix1_neg
BPix1+BPix2+FPix1_pos+FPix2_pos
```

BPix1+BPix2+FPix1_neg+FPix2_neg BPix1+FPix1_pos+FPix2_pos+FPix3_pos BPix1+FPix1_neg+FPix2_neg+FPix3_neg

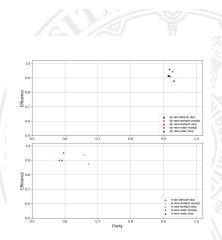
Hit-Skipping

- When NHitElectronSeedProducer was implemented for HLT, hit skipping was not added.
- Consider an example configuration where we are generating first quadruplet, then triplet, and then finally doublet seeds, masking hits along the way.
- If we require at least 3 matched hits, the old method with hit skipping would create a seed of hits BPIX1, BPIX2, BPIX3.
- But new method *without* hit skipping wouldn't make any seed from these hits.
- The "hack" is to create seeds using only steps tripletElectronSeeds, and pixelPairElectronSeeds with no masking.
- Adding skipping and removing the hack would reduce cpu time from redundant seeds.



HIT-SKIPPING - PERFORMANCE

- Enabling hit skipping and removing hack reduces number of seeds by 35% to 50%.
- 3-5x fewer seeds with respect to old seeding
- Efficiency reduced by between 4% and 6% to align more with old seeding performance.
- ► Purity improved by between about 1%.
- (table in backup)



Process	Seeding Method	< N _{seeds} >(no-skip)	$< N_{seeds} >$ (with-skip)	Percent Reduction
tī	Old - default settings	-	12.69	1 and
	New - HLT settings	4.40	2.56	41%
	New - "wide" settings	7.28	4.65	36%
Drell-Yan	Old - default settings	-	11.40	
	New - HLT settings	4.70	2.32	51%
	New - "wide" settings	5.38	2.65	51%

Adding Pileup

- The simhit-rechit linkage that was previously used in efficiency/purity measurements is not saved in GEN-SIM-RAW.
- ► Therefore, the DIGI step was re-run, but only for the signal event.
- However, running this instead of the RAW2DIGI step discarded the previously mixed pileup in the RAW.
- So even though there is a PileupInfo collection with reasonable values, there's no actual pileup hits being used for tracking (caused quite some confusion for me).
- In the end, abandon simhit-rechit linkage and just use ΔR matching for efficiency/purity.
- Some technical details in backup.

Conclusions & Outlook





BACKUP

Definitions

- Sim-Track A track from a simulated electron both originating from the luminous region of CMS (beam-spot +- 5σ) and having $|\eta| < 3.0$.
- ECAL-Driven Seed A seed created via a matching procedure between Super-Clusters and General Tracking Seeds (Either from ElectronSeedProducer or ElectronNHitSeedProducer). Must have HOE < 0.15.</p>
- ► GSF Track A track from GSF-Tracking resulting from an ECAL-Driven Seed
- GSF Tracking Efficiency The fraction of Sim-Tracks that have a matching GSF Track (based on ΔR matching)
- ► GSF Tracking Purity The fraction of GSF Tracks that have a matching Sim-Track
- GSF Tracking Fake Rate The fraction of nontruth-matched Super-Clusters which result in at least one GSF Track.

MATCHING WINDOW PARAMETERS

		narrow	default (HLT)	wide	extra-wide
Hit 1	dPhiMaxHighEt	0.025	0.05	0.1	0.15
	dPhiMaxHighEtThres	20.0	20.0	20.0	20.0
	dPhiMaxLowEtGrad	-0.002	-0.002	-0.002	-0.002
	dRzMaxHighEt	9999.0	9999.0	9999.0	9999.0
	dRzMaxHighEtThres	0.0	0.0	0.0	0.0
	dRzMaxLowEtGrad	0.0	0.0	0.0	0.0
Hit 2	dPhiMaxHighEt	0.0015	0.003	0.006	0.009
	dPhiMaxHighEtThres	0.0	0.0	0.0	0.0
	dPhiMaxLowEtGrad	0.0	0.0	0.0	0.0
	dRzMaxHighEt	0.025	0.05	0.1	0.15
	dRzMaxHighEtThres	30.0	30.0	30.0	30.0
	dRzMaxLowEtGrad	-0.002	-0.002	-0.002	-0.002
Hit 3+	dPhiMaxHighEt	0.0015	0.003	0.006	0.009
	dPhiMaxHighEtThres	0.0	0.0	0.0	0.0
	dPhiMaxLowEtGrad	0.0	0.0	0.0	0.0
	dRzMaxHighEt	0.025	0.05	0.1	0.15
	dRzMaxHighEtThres	30.0	30.0	30.0	30.0
	dRzMaxLowEtGrad	-0.002	-0.002	-0.002	-0.002

NHit Seeding window parameters. Bold designates modified values.

SAMPLES

- /ZToEE_NNPDF30_13TeV-powheg_M_120_200/RunIISummer17DRStdmix-NZSFlatPU28to62_92X_upgrade2017_realistic_v10-v1
- /TT.TuneCUETP8M2T4_13TeV-powheg-pythia8/RunIISummer17DRStdmix-NZSFlatPU28to62_92X_upgrade2017_realistic_v10-v2

CMSDRIVER INVOCATIONS

$\texttt{RAW} {\rightarrow} Step2 \ (old)$

```
cmsDriver.py RAW2Step2 \
    --mc \
    --conditions 92X_upgrade2017_realistic_v10 \
    --era Run2_2017 \
    --eventcontent FEVTDEBUG \
    -datatier GEN-SIM-DIGI-RAW \
    --step DIGI:pdigi_valid,L1,DIGI2RAW \
    --geometry DB:Extended \
    --filein file:input.root \
    --fileout file:step2.root \
    --rinluscheduled
```

Step2→TrackingNtuple (old)

```
cmsDriver.py Step2ToTrackingNtuple \
    --mc \
    --mc \
    --conditions 92X_upgrade2017_realistic_v10 \
    --erea Run2_2017 \
    --ereatcontent RECOSIM_MINIAODSIM_DQM \
    --datatier GEN-SIM-RECO_MINIAODSIM_DQMIO \
    --datatier GEN-SIM-RECO_BI_PAT_VALIDATION:@standardValidation+@miniAODValidation \
    --file in file:step2.root \
    --fileout file:trackingNtuple.root \
    --customise Validation/RecoTrack/customiseTrackingNtuple.customiseTrackingNtuple \
    --runUnscheduled
```

RAW→TrackingNtuple (new)

```
cmsDriver.py RAW2TrackingNtuple \
    --mc \
    --mc \
    --conditions 92X_upgrade2017_realistic_v7 \
    --era Run2_2017 \
    --eventcontent FEVTDEBUG \
    --datatier GEN-STM-RECO \
    --step RAW2DIGI,RECO,EI,PAT,VALIDATION \
    --customise Validation/RecoTrack/customiseTracking
.customiseTrackingNtuple \
    --filein file:input.root \
    --fileout file:trackingNtuple.root \
    --rinUnscheduled
```